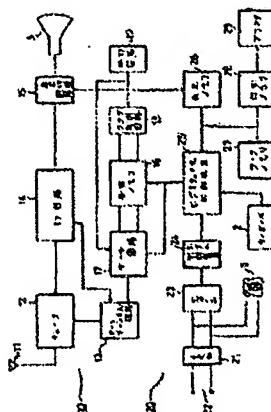


TELEVISION RECEIVER

Publication number: JP62060377
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Inventor: NAKAZAWA EIJI; TSUKAMOTO AOHIRO
Applicant: CASIO COMPUTER CO LTD
Classification:
- International: H04N7/00; H04N5/44; H04N7/173; H04N7/00; H04N5/44; H04N7/173; (IPC1-7): H04N5/44; H04N7/00; H04N7/173
- European:
Application number: JP19850200033 19850910
Priority number(s): JP19850200033 19850910

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PURPOSE: To efficiently search information on programs by providing a means for storing program information including data on broadcasting stations, broadcasting times and the types of programs and a circuit counting the current data, setting a flag to information on the program after termination or during broadcasting and referring to the flag so as to search the program. **CONSTITUTION:** When a flag control circuit 18 detects pulses for a minute out of a clock circuit 19, it takes an indicated address N corresponding to a corresponding data area in a program memory 16 for a top address (x), and reads out the stored contents. If the program is broadcasted right now, a broadcasting flag F1 is set. When the time reaches the broadcast end, the circuit 18 sets a broadcast termination flag F2 and resets the flag F1. When the current time is before the broadcast starting time, a memory indication address N is added with a positive '1', a similar action is repeated. The flags F1 and F2 are set when the program is broadcasted and when it is terminated, respectively. After the operation handling an area dated on a date concerned in the program memory 16 is finished, the circuit 18 waits until clock circuit 19 transmits the next pulses for a minute.



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